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Network synthesis is a design technique for linear electrical circuits. Synthesis starts from a prescribed impedance function of frequency or frequency response and then determines the possible networks that will produce the required response. The technique is to be compared to network analysis in which the response of a given circuit is calculated. Network synthesis was a great leap forward in circuit design. Prior to network synthesis, only network analysis was available, but this requires the

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As other reviewers have stated, this book and Van Valkenburg's book "Network Analysis" are 2 of the best books out there, on a curious note: it is interesting that Franklin F. Kuo was Van Valkenburg's PhD student, and both ended up writting some of the best books in the EE field, Kuo is more recognized in the control theory field whilst Van ...

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